



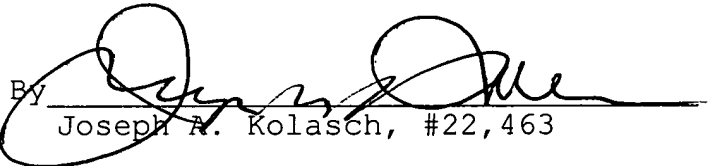
Appl. No. 09/813,834

- ☒ Petition for one (1) month(s) extension of time pursuant to 37 C.F.R. §§ 1.17 and 1.136(a). \$110.00 for the extension of time.
- ☐ No fee is required.
- ☒ Check(s) in the amount of \$110.00 is(are) enclosed.
- ☐ Please charge Deposit Account No. 02-2448 in the amount of \$0.00. This form is submitted in triplicate.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

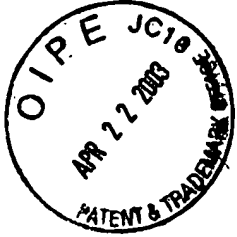
By   
Joseph A. Kolasch, #22,463

SB  
JAK/SB/mua  
0465-0826P

P.O. Box 747  
Falls Church, VA 22040-0747  
(703) 205-8000

Attachment(s)

(Rev. 10/15/02)



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TRANS.  
PATENT  
0465-0826P  
Jim Kimball  
4/29/03

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicants: Jae-Sung PARK et al. Conf.: 3138  
Serial No.: 09/813,834 Art Unit: 2811  
Filed: March 22, 2001 Examiner: D. Owens  
For: SEMICONDUCTOR DEVICE HAVING SIDEWALL  
SPACERS MANIFESTING A SELF-ALIGNED  
CONTACT HOLE

RECEIVED  
APR 25 2003  
TECHNOLOGY CENTER 2800

AMENDMENT UNDER 37 C.F.R. §1.111

Assistant Commissioner for Patents  
Washington, DC 20231

April 22, 2003

Sir:

In response to the Office Action mailed December 23, 2002 (Paper No. 10), the due date for response having been petitioned to be extended one (1) month to April 23, 2003, the following amendments and remarks are respectfully submitted in connection with the above-identified application:

IN THE CLAIMS:

Please **rewrite claim 5** as follows:

5. (Amended) An unsymmetrical semiconductor device using a self-aligned contact hole, the device comprising:

a substrate having impurity regions formed therein;

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